

ABSTRACT

A liquid crystal display apparatus having a level conversion circuit in which a high speed operation can be attained and also a small transistor capacity can be obtained is provided. In the liquid crystal display apparatus, a signal circuit for driving pixel elements of a display unit and a scanning circuit are provided. The level conversion circuit is constituted of a first and a second transistors 111 and 112 in which a respective gate electrodes is connected to a first bias voltage power supply, and a third and a fourth transistors 121 and 122 in which a respective gate electrodes is connected to a second bias voltage power supply and a respective source electrodes is connected to a power supply.